# DES Decryption Demistified 

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## 1 Introduction

DES provides an excellent sample of symmetric encryption for students. We provides theoretical and practical instruction for the implementation of DES in C $\sharp$. For instruction purposes we use the original 56-bit keys and 64-bit data blocks. However, we do not consider block chaining. We examine at DES from an outside in approach describing the algorithm at a high level and then taking more detailed looks at each part. Section 2 describes the elements of the algorithm. Section 3 takes an in-depth at each of the operations. Section 4 develops the concepts for students will need to code the algorithm for the class.

## 2 Theory from the outside in

DES uses the Feistal Cypher to encrypt and decrypt messages with the same key. The basic structure encrypts one 64-bit block of data at a time. Each block is encrypted by the following steps shown in Figure 1

1. An initial permutation
2. Multiple rounds (16) that repeatedly modify the block using a a function of the key
3. A final 32-bit swap
4. Inverse of the initial permutation

To start we have the Data $D$ permuted as follows.

$$
D^{\prime}=I P(D)
$$

Then it is seperated into two parts such that

$$
D^{\prime} \rightarrow L_{0} \| R_{0}
$$

Each round uses the left and right 32-bit halves from the previous step and a unique 56-bit key derived from the 64-bit key given by the user. Hence we generate a unique


Figure 3.4 General Depiction of DES Encryption Algorithm

Figure 1: High-level view of the Feistal Cipher.
key for each of the 16 rounds. The key generation algorithm does not depend on the encryption and can execute before the operations performed in the rounds. Your algorithm should perform this step and store the keys for later use when encrypting multiple blocks. (i.e. for data that contains more than 64 bits, break up the data into 64-bit blocks where each block must go through the same encryption using the same keys.)

The rounds themselves are fairly straightforward. The $i^{\text {th }}$ round is given by the following.

1. Input of a Left 32-bit (labeled $L_{i-1}$ ) and right 32-bit block (labeled $R_{i-1}$ ).
2. The new left output $L_{i}=R_{i-1}$
3. The new right input $R_{i}=L_{i-1} \oplus F\left(R_{i-1}, K_{i}\right)$

Figure 2 shows the single round for both the block encryption and the key generation. The number of rounds in DES is not important to see that decryption works exactly the same way except with inverted key order.


Figure 3.5 Single Round of DES Algorithm

Figure 2: Single Round

Consider the following theoretical example that walks through the process of encryption and decryption.

Example 1 Let LO and R0 represent the left and right 32-bit halves of the block after the initial permutation. These two 32-bit blocks go through the rounds in DES as shown below where rows labeled $e_{i}$ show the results of encryption after the $i^{\text {th }}$ round and $d_{i}$ show decryption after the $i^{\text {th }}$ round.

| $e_{0}$ | $L 0$ | $R 0$ |
| :---: | :---: | :---: |
| $e_{1}$ | $R 0$ | $F(R 0, K 0) \oplus L 0$ |
| $e_{2}$ | $F(R 0, K 0) \oplus L 0$ | $F(F(R 0, K 0) \oplus L 0, K 1) \oplus R 0$ |
| $\cdots$ | $\cdots$ | $\cdots$ |
| swap | $F(F(R 0, K 0) \oplus L 0, K 1) \oplus R 0$ | $F(R 0, K 0) \oplus L 0$ |
| $\ldots$ | $\cdots$ | $\cdots$ |
| $d_{0}$ | $F(F(R 0, K 0) \oplus L 0, K 1) \oplus R 0$ | $F(R 0, K 0) \oplus L 0$ |
| $d_{1}$ | $F(R 0, K 0) \oplus L 0$ | $F(F(R 0, K 0) \oplus L 0, K 1) \oplus F(F(R 0, K 0) \oplus L 0, K 1) \oplus R 0$ |
| $d_{1}$ | $F(R 0, K 0) \oplus L 0$ | $R 0$ |
| $d_{2}$ | $R 0$ | $F(R 0, K 0) \oplus R(R 0, K 0) \oplus L 0$ |
| $d_{2}$ | $R 0$ | $L 0$ |
| $S w a p$ | $L 0$ | $R 0$ |

The left and right sides are combined to get $D^{\prime}$ and the decrypted dated is given by

$$
D=I P^{-1}\left(D^{\prime}\right)
$$

Including more rounds will produce much more complicated expressions at each level, but the decryption process is identical. More rounds may be added in the ellipses. Adding more rounds would require an elipses before and after the swap ending the encryption part of table.

This gives a good overview of the flow of operations in the DES algorithm, but it does not tell us much about the details of each operation. Let us consider that details next.

## 3 Operations In-depth

Consider Figure 2. The operations shown there and in the previous section consist of:

1. IP, initial permutation that happens before the rounds in DES.
2. E, Expansion Permutation (in F )
3. S-Box, Substitution Choice (in F)
4. P , permutation (in F )
5. P2, permutation choice 2 (for key generation)
6. XOR (in various places in the round)
7. Left Shift (for key generation)

### 3.1 Permutations: All Types

All permutations behave identically even though they may reduce or expand the original input. Consider the following useful definition of a permutation.

Definition 2 Let $A$ be a binary string of length $a$ and let $B$ be a binary string of length b. A permutation consists of a integer array identified as $P$ of length $b$ such that the number in the $i^{\text {th }}$ position of $P$ identifies the value at the position in $A$ that should be copied to the $i^{\text {th }}$ position in B. Graphically center block in Figure 3 represents the permutation.

In an expansion permutations, elements from the input must appear multiple times in the output. Whereas in contraction permutations, elements from the input may not appear in the output.


Figure 3.1 General $n$-bit- $n$-bit Block Substitution (shown with $n=4$ )

Figure 3: Permutation

### 3.2 S-Box Substitutions

Figure 4 shows the $F$ function logic that occurs in each round of DES. The expansion takes the right 32 -bit half and expands it to 48 . This result is then XOR'd with the 48bit round key. The 48 -bit result of the XOR is then input into the mysterious S-Boxes. As we see in Figure 4, each S-Box takes 6 bits as input and outputs 4 bits.

S-Boxes are no more than plain ordinary tables. The output of an S-Box is determined by the row and column that the input specifies. The row is identified by concatenating the first bit to the last bit to yield a two bit number indicating row 0,1 , 2 , or 3 . The middle 4 bits are used as a number $0,1, \ldots, 15$ to identify the column. The four bits that are in that intersection of the row and column are the output of the S-Box.

### 3.3 XOR and Shift

These last two operations are extremely simple to perform on unsigned integers and we leave a discussion of these two operators to the next section.


Figure 3.6 Calculation of $\mathrm{F}(\mathrm{R}, \mathrm{K})$

Figure 4: The F function

## 4 Coding Considerations and the Assignment

This programming assignment must be designed and built using test driven design. That means you need to build each individual piece of functionality and test it thoroughly before integrating it with the rest of the program. Trust me, you will save yourself hours of troubleshooting by following this model.

To implement test driven design you may choose to download and install Visual Studio Team System 2008 Team Suite from MSDNAA from our computing resources website. MSDN's unit testing walkthrough gives an extensive document that you may find useful.

I will grade your program based on an interface called IDesGradable that you will find along with other requirements at the class wiki.

## Hints:

For S-Boxes use an array of two dimensional arrays to store the actual S-Boxes. Then the first index identifies the S-Box, The second index identifies the row and the last index identifies to column.

For permutations you probably should pass in the input and an array that maps the input to the output as discussed above. Standardizing on left or right justifying your output and input will significantly simplify your life. I'll leave it to you to determine which fits best for you.

Numerous operations require you to select a specific bit from an unsigned long. It may be worth your time to code a simple move bit operation that takes your unsigned long and a input position and output position. The function should return the input position value OR'd in the proper output with zero. That way you possibly have just one bit on in the output and it is located at the proper location.

